# ELC 406A Advanced Digital Communication

Lecture 6 Synchronization

#### Synchronization

- Carrier Synchronization
- Bit Synchronization
- Code Synchronization



## Synchronization

#### Acquisition

- Coarse tunning
- Once (start)
- No data
- accepted error = +/- Tc

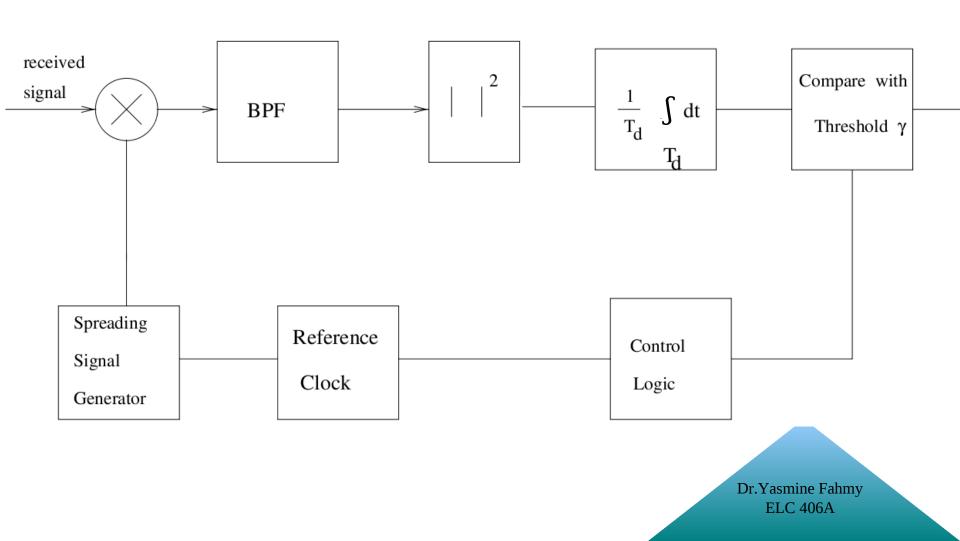
#### Traking

- fine tunning
- All the time
- with data
- accepted error
  < +/- Tc</li>

#### Two different errors:

#### → False Alarm

#### → Miss



Two important parameters: → Window length: Td → Threshold: γ

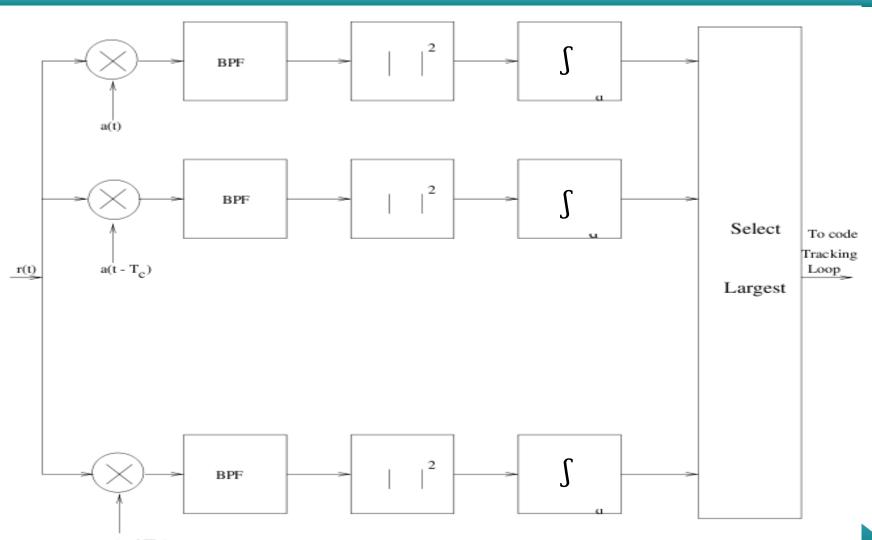
Different search techniques:

Serial search

Parallel search

Hybrid search

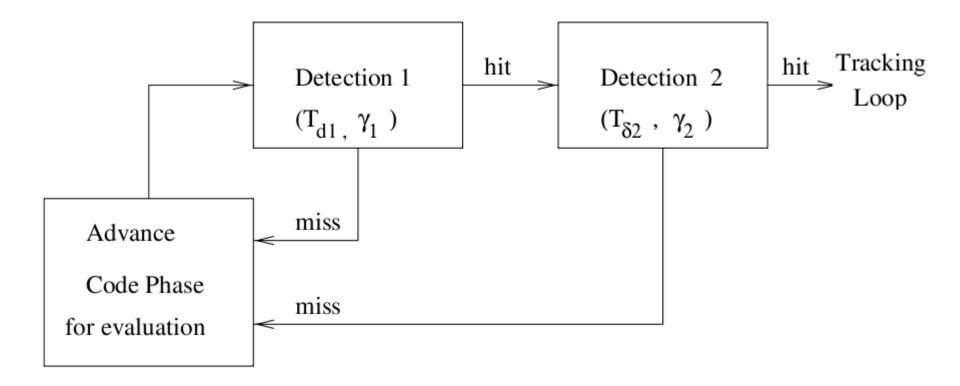
#### **Acquisition : Parallel**



a(t - NT c)

Different detection techniques: → Single dwell → Multi dwell

#### Acquisition : Multidwell



First detection stage: designed to have a low threshold and a short integration time such that the miss probability is small but the false alarm probability is high

Second stage: designed to have small miss and false alarm probabilities. In this stage only the most probable cells (from the first stage) are tested.

## Tracking

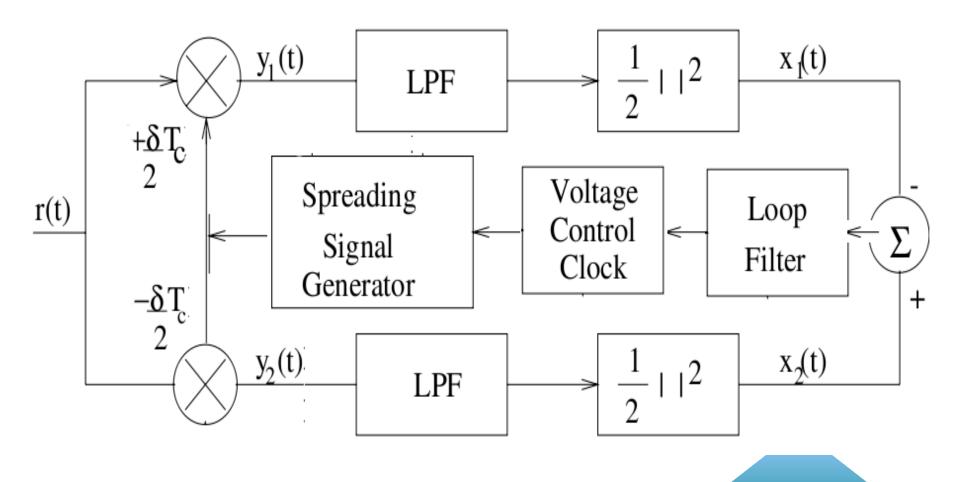
Two Different techniques:

- Delay locked loop
- (Early Late gate)
- → Tau-Dither

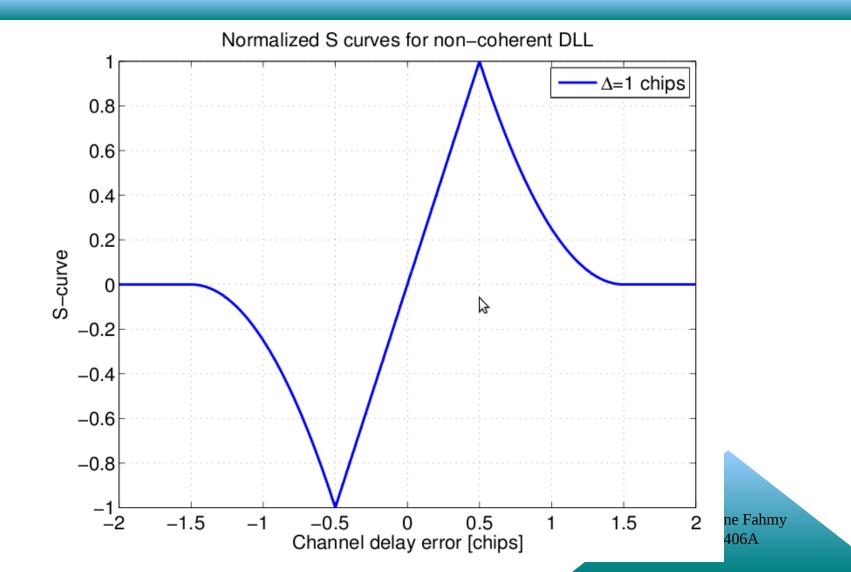
DSSS

FHSS

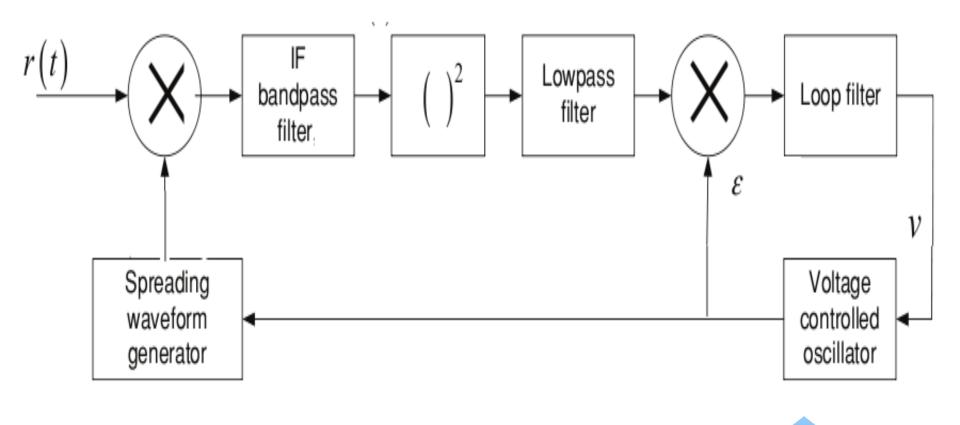
## Tracking : DLL



## Tracking : DLL



### Tracking : Tau-Dither



# Questions ???

# Thank You